#### THE AUSTRALIAN NATIONAL UNIVERSITY

Mid Semester Examination, September 2013

# COMP2310 / COMP6310 (Concurrent and Distributed Systems )

Writing Period: 1 hour duration

Study Period: 0 minutes duration

Permitted Materials: One A4 page with handwritten notes on both sides.

NO calculator permitted.

Questions are NOT equally weighted. This exam will contribute 10% to your final assessment.

The questions are followed by labelled, framed blank panels into which your answers are to be written. Additional answer panels are provided (at the end of the paper) should you wish to use more space for an answer than is provided in the associated labelled panels. If you use an additional panel, be sure to indicate clearly the question and part to which it refers to.

The marking scheme will put a high value on clarity so, as a general guide, it is better to give fewer answers in a clear and concise manner than to outline a greater number in a sketchy, half-answered fashion.

Please write clearly - if we cannot read your writing you will lose marks!

Student Number	(please write	e clearly):		Enrollment (circle one):
				COMP2310
				COMP6310
Official use only:				
Q1 (14) Q2 (14)	Q3 (17)	Q4 (5)	Total (50)	

# QUESTION 1 [14 marks]

(a)	Briefly describe the role of the run-time stack in the implementation	on of threads.
	QUESTION 1(a)	[2 marks]
<b>(b)</b>	In the implementation of Java threads, describe the relationship and run() methods.	between the start()
	QUESTION 1(b)	[2 marks]
(c)	List the various ways to prevent deadlocks.	
	QUESTION 1(c)	[4 marks]

Que	estion 1 (continued)	Student Number:
( <b>d</b> )	Define the terms <i>safety</i> and <i>liven</i> of a concurrent program.	ness. Briefly explain how these relate to the correctness
	QUESTION 1(d)	[3 marks]
(e)	In concurrent programming, there and from model to implementatio ologies for constructing concurre	e exist methodologies to go from requirements to model on. Why is it particularly important to apply such methodent systems?
	QUESTION 1(e)	[3 marks]

## QUESTION 2 [14 marks]

(	ัล	) List all	traces	of len	oth 2	for the	<b>FSP</b>	process:
١	u	, List an	uaces	OI ICII	5111 2	TOT THE	1 1	process.

SUM = (in[a:0..1][b:0..1] -> TOTAL[a+b]), TOTAL[s:0..2] = (out[s] -> SUM).

QUESTION 2(a)	[2 marks]

(b) How many states does the process SUM have?

QUESTION 2(b)	[1 mark]

- (c) A *binary semaphore*, in its initial state, can accept an **down** action, after which it can only accept a **up** action and returns to its initial state.
  - (i) Write an FSP model for the binary semaphore in the process BSEM.

```
QUESTION 2(c)[i] [2 marks]
```

(ii) An agent using a binary semaphore named **mutex** to guard the repeated **entry** and **exit** of a critical region may be modelled by:

```
AGENT = (mutex.down->enter->exit->mutex.up->AGENT).
```

Write an FSP parallel composition, **BSEMDEMO**, of a system with two agent processes and a binary semaphore such that only one agent can be in the critical region at any time.

QUESTION 2(c)[ii]	[3 marks]

<b>Question 2 (continued)</b>	Student Number:
(iii) Draw a Labelled Transition System ( property MUTEX = (a[i:12]	LTS) for the FSP property: .enter -> a[i].exit -> MUTEX).
QUESTION 2(c)[iii]	[3 marks]
system of Q2(c)(ii) to include the proform renaming of the action prefixes of Would you expect the property to be <b>COMP6310 students</b> , answer the form FSP progress property to express the critical region. Would you expect	collowing: Specify an FSP composition of the perty MUTEX (noting that you may need to per of MUTEX, depending on your previous answer) violated? Briefly explain.  collowing: For the system of Q2(c)(ii), specify either thread will eventually be allowed to ente this property to be violated? Briefly explain sing action priority that could check this.  [3 marks]

## QUESTION 3 [17 marks]

QUESTION 3(a)		which thread(s) makes the transition.  [3 mar]
dition? Include in yo	our answer 15 lines or less of	nin what is meant by the term <i>race</i> of pseudo code that has a race condit is <i>interference between threads</i> .
QUESTION 3(b)		[5 mar

### **Question 3 (continued)**

(c) Consider the following Java code fragment of a monitor:

```
class AMonitor {
    private boolean aState = false; ...
    synchronized void accessMethod1() {
        while (aState == false) wait(); ...
    }
}
```

(i) Assuming that the monitor functioned as intended, what would be the effect of replacing the loop in accessMethod1() with a spin-loop,
 i.e. while (aState == false) {/\*spin\*/}

```
QUESTION 3(c)[i] [2 marks]
```

(ii) How could you implement the effect of the **synchronized** keyword (i.e. governing the entry and exit of a synchronized method) using a test-and-set operation? This operation is defined as follows:

```
int testAndSet(volatile int *Lock):
{int lv = *Lock; *Lock = 1; return lv;} (atomically)
```

```
QUESTION 3(c)[ii] [3 marks]
```

ive example using pseudo-code.  QUESTION 3(d)		[4 ma
Additional answers to QUESTION	_(_)[_]	

STION 4(a)					[3 mark
one advantage	and one di	sadvantag	e of asynchi	ronous messa	ge passing over s
ous message p	assing.				[2 marl
	one advantage ous message p STION 4(b)	ous message passing.	ous message passing.	ous message passing.	

Student Number: .....

Additional answers to QUESTION()[]
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